FIG 1a

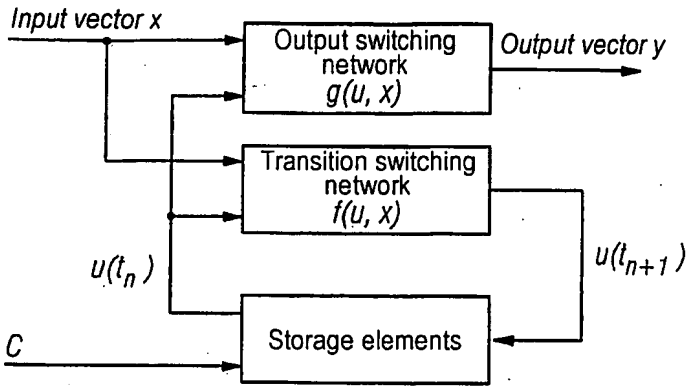


FIG 1b

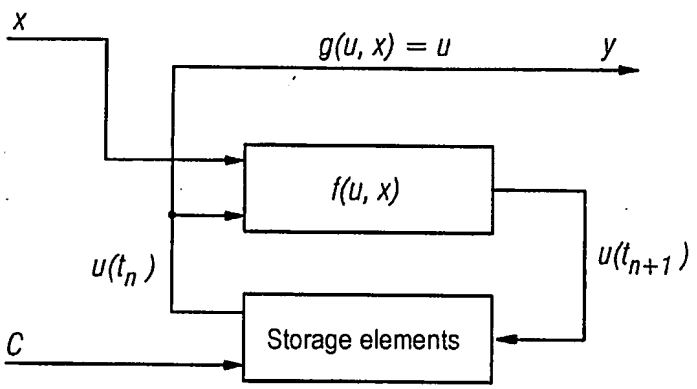
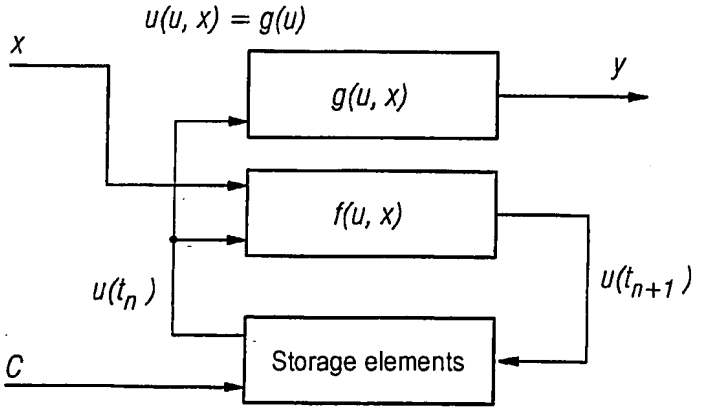


FIG 1c



10/532643

FIG 1d

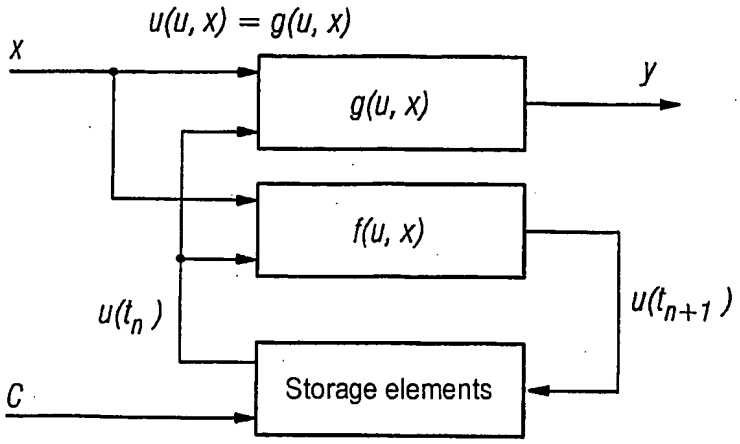


FIG 2

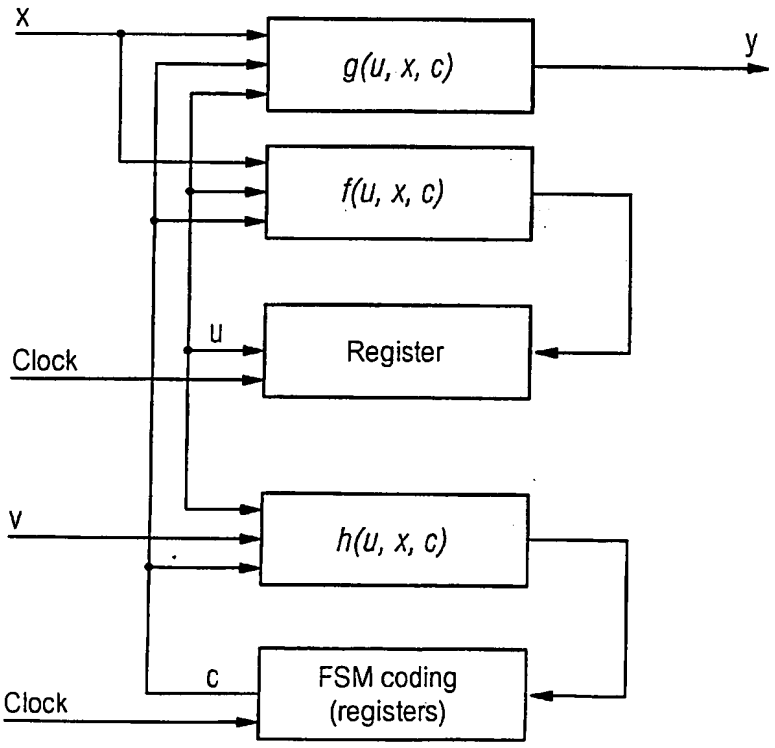


FIG 3

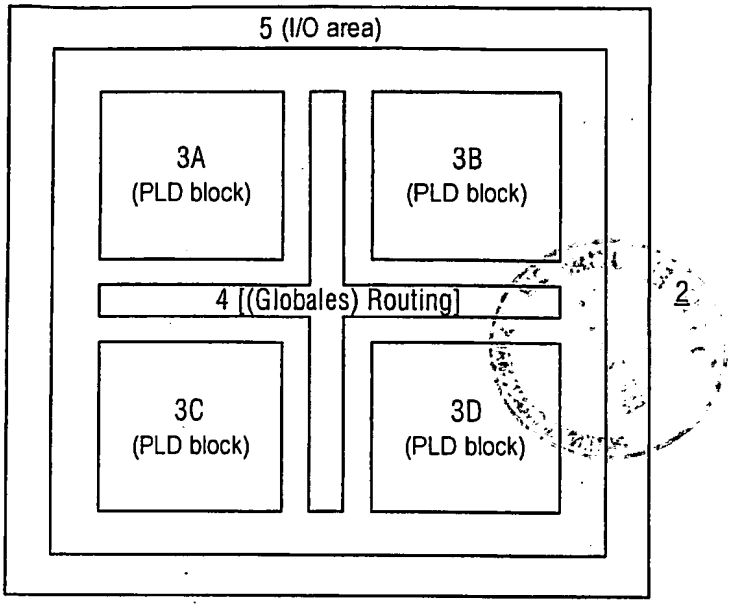
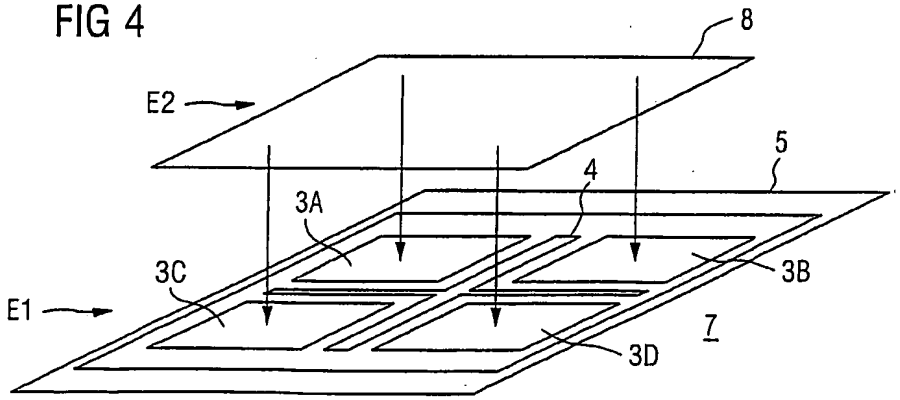

5 (I/O area)
3A
(PLD block)
3B
(PLD block)
4 [(Globales) Routing]
2
3C
(PLD block)
3D
(PLD block)

FIG 4


8
E2
5
4
3A
3C
3B
E1
7
3D